

# ASMB-MTB1-0A3A2

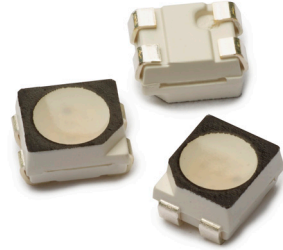
## PLCC-4 Tricolor Black Surface LED



### Data Sheet



Lead (Pb) Free  
RoHS 6 fully  
compliant



#### Description

This family of SMT LEDs are in PLCC-4 package. A wide viewing angle together with the built in reflector drives up the intensity of light output making these LEDs suitable for use in interior electronics signs. The black top surface of the LED provides better contrast enhancement, especially in full color display.

These LEDs are compatible with reflow soldering process. For easy pick & place, the LEDs are shipped in tape and reel. Every reel is shipped from a single intensity and color bin except red color for better uniformity.

#### Features

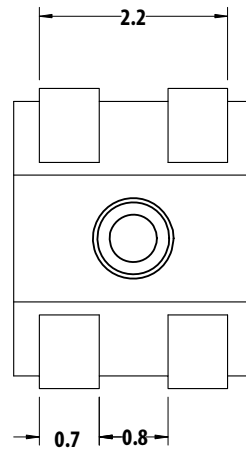
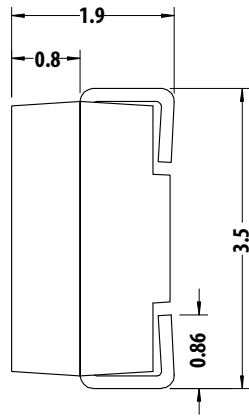
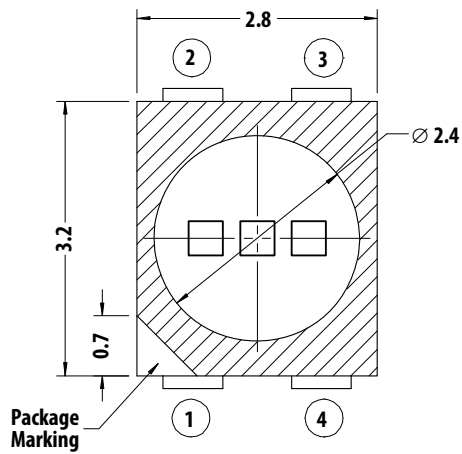
- Standard PLCC-4 package (Plastic Leaded Chip Carrier)
- LED package with diffused silicone encapsulation
- Using AlInGaP and InGaN dice technologies
- Typical viewing angle at 115°
- Compatible with reflow soldering process
- JEDEC MSL 3

#### Applications

- Indoor full color display

**CAUTION:** LEDs are ESD-sensitive. Please observe appropriate precautions during handling and processing. Refer to Avago Application Note AN-1142 for additional details.

## Package Dimensions

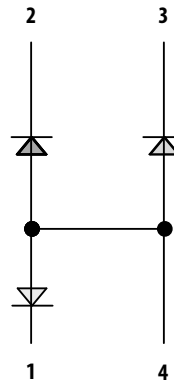


### Lead Configuration

1	Cathode (Red)
2	Cathode (Green)
3	Cathode (Blue)
4	Common Anode

### Notes:

1. All dimensions are in millimeters (mm).
2. Unless otherwise specified, tolerance =  $\pm 0.20$  mm.
3. Encapsulation = silicone
4. Terminal Finish: Silver plating



**Table 1. Absolute Maximum Ratings (T<sub>J</sub> = 25 °C)**

Parameter	Red	Green/Blue	Unit
DC Forward Current [1]	25	25	mA
Peak Forward Current [2]	100	100	mA
Power dissipation	65	90	mW
Reverse Voltage	Not recommended for reverse bias		
Junction Temperature	110		°C
Operating Temperature Range	-40 to 100		°C
Storage Temperature Range	-40 to 100		°C

Notes:

1. Derate linearly as shown in Figure 7 to Figure 10.
2. Duty factor = 10% frequency = 1 kHz.

**Table 2. Optical Characteristics (T<sub>J</sub> = 25 °C)**

Color	Luminous Intensity, I <sub>V</sub> (mcd) @ I <sub>F</sub> = 20 mA [1]			Dominant Wavelength, λ <sub>d</sub> (nm) @ I <sub>F</sub> = 20 mA [2]			Peak Wavelength, λ <sub>p</sub> (nm) @ I <sub>F</sub> = 20 mA	Viewing Angle, 2θ <sub>1/2</sub> (°) [3]
	Min.	Typ.	Max.	Min.	Typ.	Max.	Typ.	Typ.
Red	450	540	900	619.0	625.0	629.0	634.0	115
Green	1125	1600	2240	525.0	530.0	535.0	522.0	115
Blue	285	350	560	465.0	470.0	473.0	465.0	115

Notes:

1. Luminous intensity, I<sub>V</sub> is measured at the mechanical axis of the LED package at a single current pulse condition. The actual peak of the spatial radiation pattern may not be aligned with the axis.
2. Dominant wavelength is derived from the CIE Chromaticity Diagram and represents the perceived color of the device.
3. θ<sub>1/2</sub> is the off-axis angle where the luminous intensity is ½ of the peak intensity.

**Table 3. Electrical Characteristics (T<sub>J</sub> = 25 °C)**

Color	Forward Voltage, V <sub>F</sub> (V) @ I <sub>F</sub> = 20 mA [1]			Reverse Voltage, V <sub>R</sub> (V) @ I <sub>R</sub> = 100 μA [2]	Reverse Voltage, V <sub>R</sub> (V) @ I <sub>R</sub> = 10 μA [2]	Thermal Resistance, R <sub>θJ-5</sub> (°C/W)	
	Min.	Typ.	Max.	Min.	Min.	Single chip on	Three chips on
Red	1.8	2.1	2.6	4.0	-	609	653
Green	2.8	3.1	3.6	-	4.0	320	430
Blue	2.8	3.1	3.6	-	4.0	320	430

Notes:

1. Tolerance = ± 0.1 V.
2. Indicates product final testing condition. Long-term reverse bias is not recommended.

## Part Numbering System

A S M B - M T B 1 - 0 A 3 A 2

x1                      x2 x3 x4 x5

Code	Description	Option	
x1	Package type	B	Black surface
x2	Minimum intensity bin	A	Red: bin U1 Green: bin W1 Blue: bin T1
			Red: bin U1, U2, V1 Green: bin W1, W2, X1 Blue: bin T1, T2, U1
x3	Number of intensity bins	3	3 intensity bins from minimum
x4	Color bin combination	A	Red: full distribution Green: bin A, B, D Blue: bin A, B, C
x5	Test option	2	Test current = 20 mA

**Table 4. Bin Information**

### Intensity Bins (CAT)

Bin ID	Luminous intensity (mcd)	
	Min.	Max.
T1	285.0	355.0
T2	355.0	450.0
U1	450.0	560.0
U2	560.0	715.0
V1	715.0	900.0
V2	900.0	1125.0
W1	1125.0	1400.0
W2	1400.0	1800.0
X1	1800.0	2240.0

Tolerance: ±12%

### Color Bins (BIN) – Blue

Bin ID	Dominant Wavelength (nm)		Chromaticity coordinate (for reference)	
	Min.	Max.	Cx	Cy
A	465.0	469.0	0.1355	0.0399
			0.1751	0.0986
			0.1680	0.1094
			0.1267	0.0534
B	467.0	471.0	0.1314	0.0459
			0.1718	0.1034
			0.1638	0.1167
			0.1215	0.0626
C	469.0	473.0	0.1267	0.0534
			0.1680	0.1094
			0.1593	0.1255
			0.1158	0.0736

Tolerance: ±1 nm

### Color Bins (BIN) – Green

Bin ID	Dominant Wavelength (nm)		Chromaticity Coordinate (for reference)	
	Min.	Max.	Cx	Cy
A	525.0	531.0	0.1142	0.8262
			0.1624	0.7178
			0.2001	0.6983
			0.1625	0.8012
B	528.0	534.0	0.1387	0.8148
			0.1815	0.7089
			0.2179	0.6870
			0.1854	0.7867
D	531.0	535.0	0.1625	0.8012
			0.2001	0.6983
			0.2238	0.6830
			0.1929	0.7816

Tolerance: ± 1 nm

### Color Bins (BIN) – Red

Bin ID	Dominant Wavelength (nm)		Chromaticity Coordinate (for reference)	
	Min.	Max.	Cx	Cy
--	619.0	629.0	0.6894	0.3104
			0.6752	0.3113
			0.6916	0.2950
			0.7066	0.2934

Tolerance: ±1 nm

# Characteristics

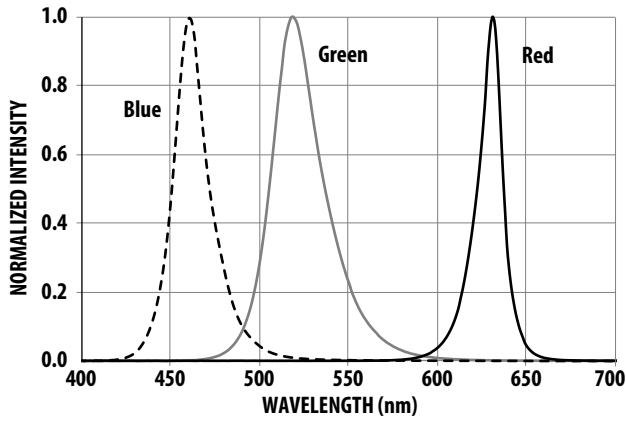


Figure 1. Relative Intensity vs. Wavelength

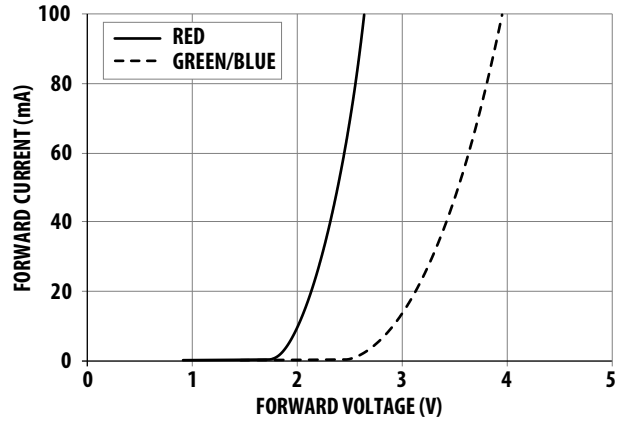


Figure 2. Forward Current vs. Forward Voltage

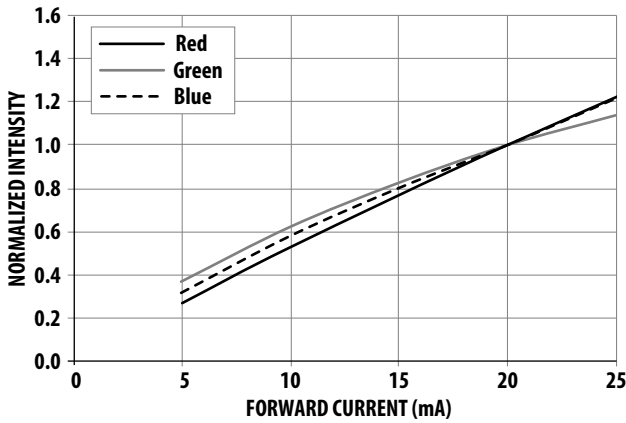


Figure 3. Relative Luminous Intensity vs. Forward Current

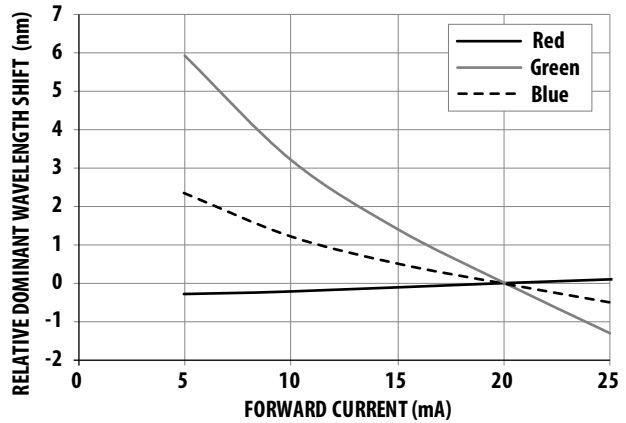


Figure 4. Dominant Wavelength Shift vs. Forward Current

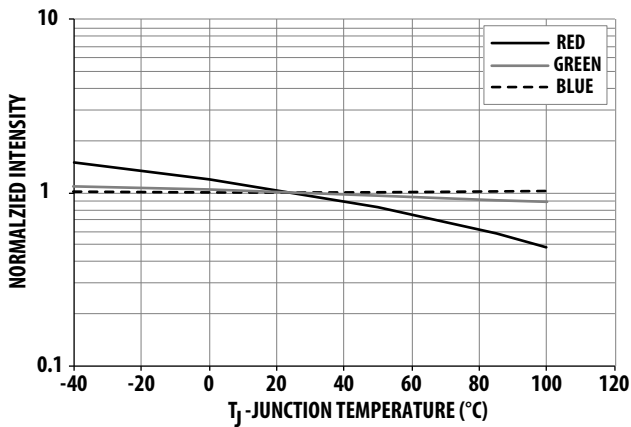


Figure 5. Relative Luminous Flux vs. Junction Temperature

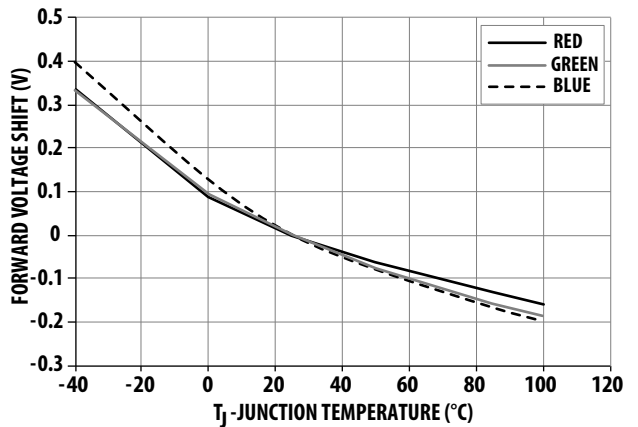


Figure 6. Forward Voltage Shift vs. Junction Temperature

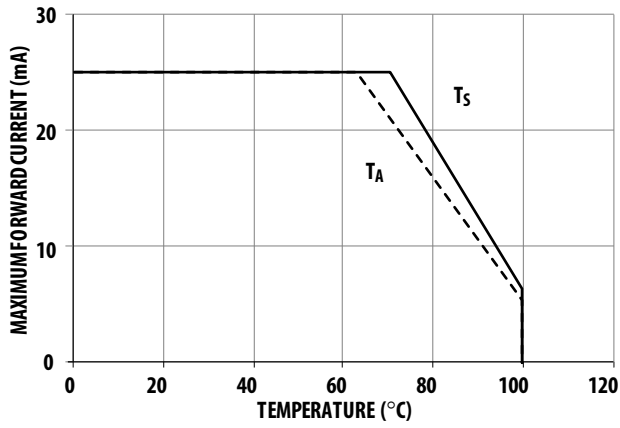


Figure 7. Maximum Forward Current vs. Temperature For Red (1 Chip On)

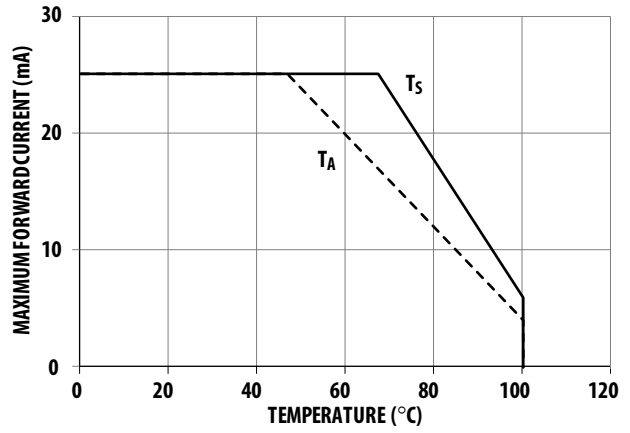


Figure 8. Maximum Forward Current vs. Temperature For Red (3 Chips On)

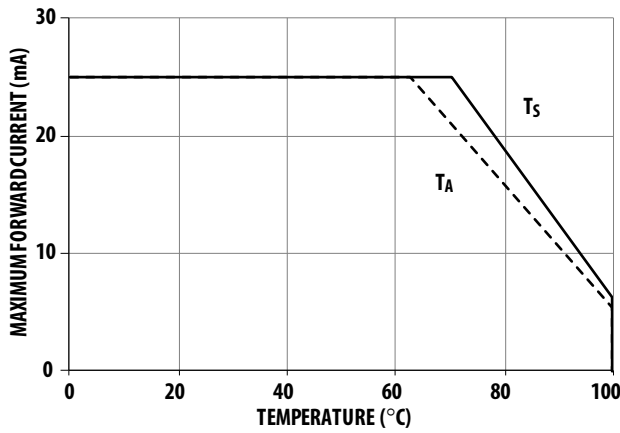


Figure 9. Maximum Forward Current vs. Temperature for Green & Blue (1 Chip On)

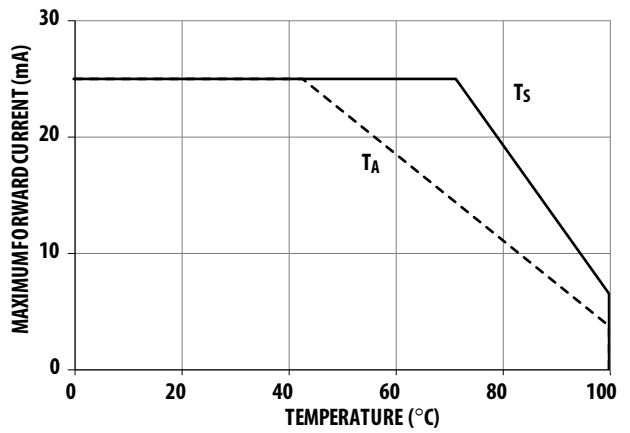


Figure 10. Maximum Forward Current vs. Temperature for Green & Blue (3 Chips On)

Note:

Maximum forward current graphs based on ambient temperature,  $T_A$  are with reference to thermal resistance  $R\theta_{J-A}$  (see below). For more details, see Precautionary Notes, item 4.

Condition	Thermal resistance from LED junction to ambient, $R\theta_{J-A}$ ( $^{\circ}C/W$ )	
	Red	Green & Blue
1 chip on	725	454
3 chips on	970	747

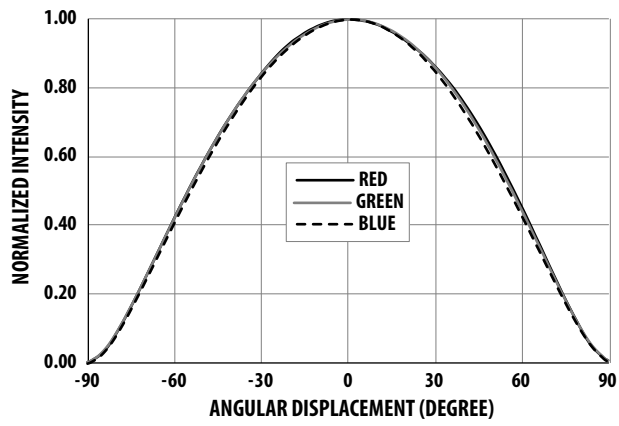


Figure 11a. Radiation pattern along x-axis of the package

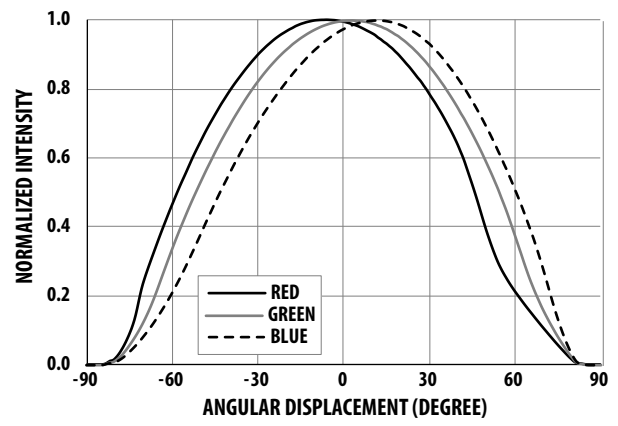


Figure 11b. Radiation pattern along y-axis of the package

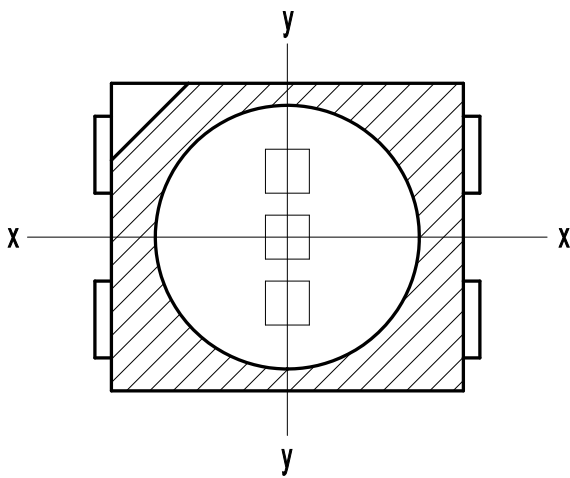


Figure 11c. Illustration of package axis for radiation pattern

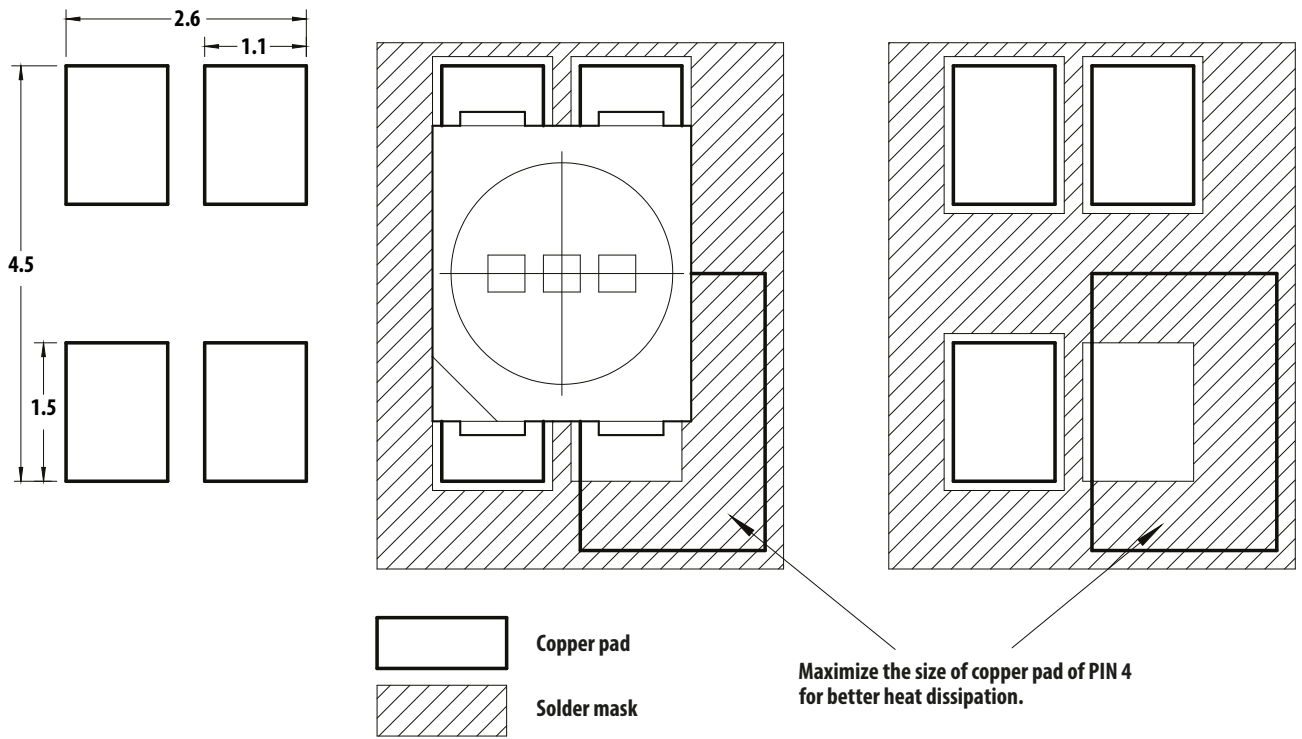


Figure 12. Recommended soldering land pattern

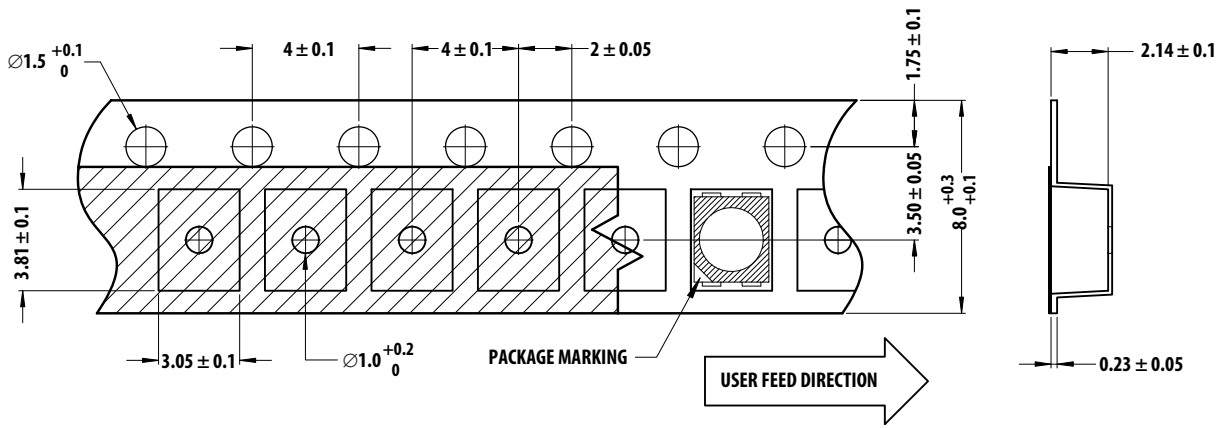


Figure 13. Carrier Tape Dimension



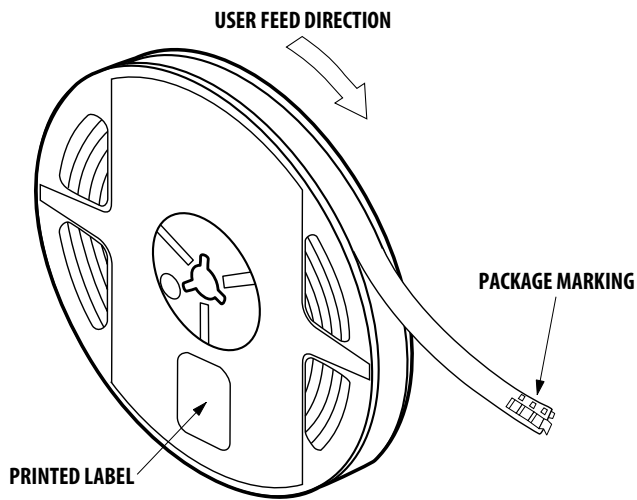


Figure 14. Reel Orientation

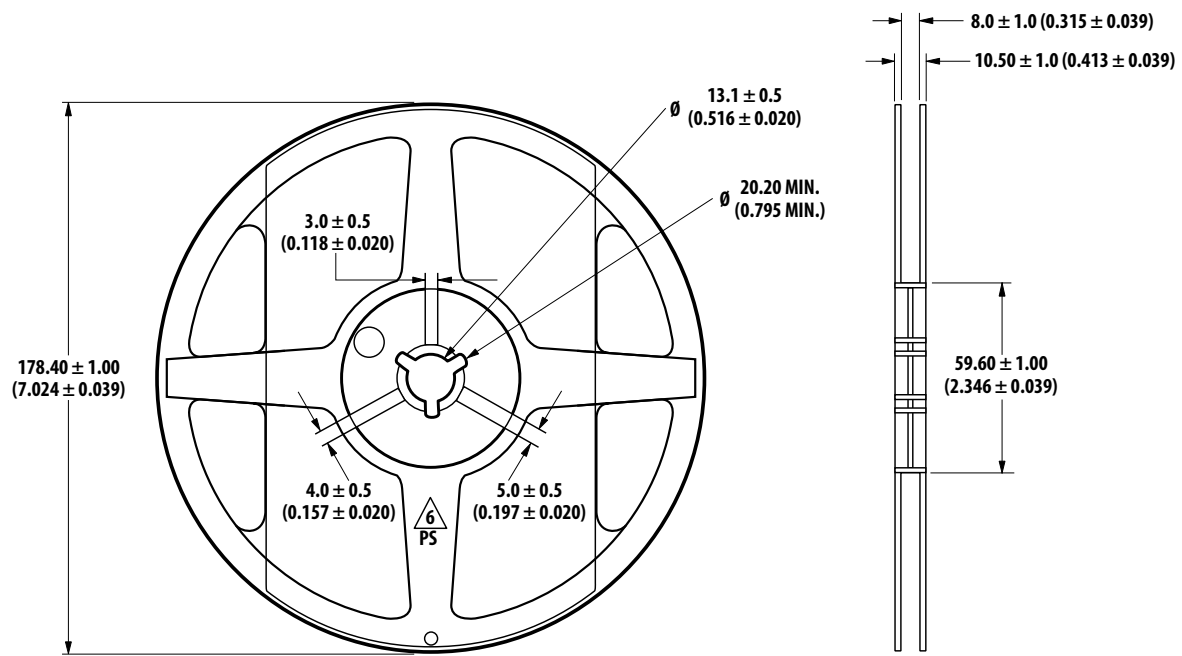
















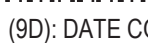
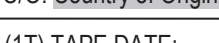


Figure 15. Reel Dimension

## Packing Label

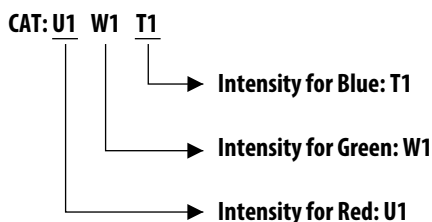
(i) Standard label (attached on moisture barrier bag)

<p>(1P) Item: <b>Part Number</b>  </p>		<p><b>AVAGO</b>  <b>TECHNOLOGIES</b>          STANDARD LABEL LS0002          RoHS Compliant Halogen Free          e4 Max Temp 260C MSL3</p>	
<p>(1T) Lot: <b>Lot Number</b>  </p>		<p>(Q) QTY: <b>Quantity</b>  </p>	
<p>LPN:  </p>		<p>CAT: <b>Intensity Bin</b>  </p>	
<p>(9D)MFG Date: <b>Manufacturing Date</b>  </p>		<p>BIN: <b>Color Bin</b>  </p>	
<p>(P) Customer Item:  </p>			
<p>(V) Vendor ID:  </p>		<p>(9D) Date Code: <b>Date Code</b>  </p>	
<p>DeptID:  </p>		<p>Made In: <b>Country of Origin</b>  </p>	

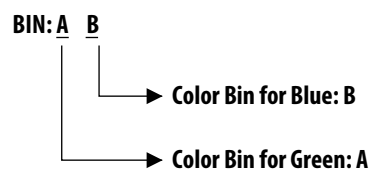
(ii) Baby label (attached on plastic reel)

<p>(1P) PART #: <b>Part Number</b>  </p>		<p><b>AVAGO</b>  <b>TECHNOLOGIES</b>          BABY LABEL COSB001B V0.0</p>	
<p>(1T) LOT #: <b>Lot Number</b>  </p>		<p>QUANTITY: <b>Packing Quantity</b>  </p>	
<p>(9D)MFG DATE: <b>Manufacturing Date</b>  </p>		<p>(9D): DATE CODE:  </p>	
<p>C/O: <b>Country of Origin</b>  </p>		<p>D/C: <b>Date Code</b> VF:  </p>	
<p>(1T) TAPE DATE:  </p>		<p>CAT: <b>INTENSITY BIN</b>          BIN: <b>COLOR BIN</b></p>	

Example of luminous intensity (lv) bin information on label:



Example of color bin information on label:

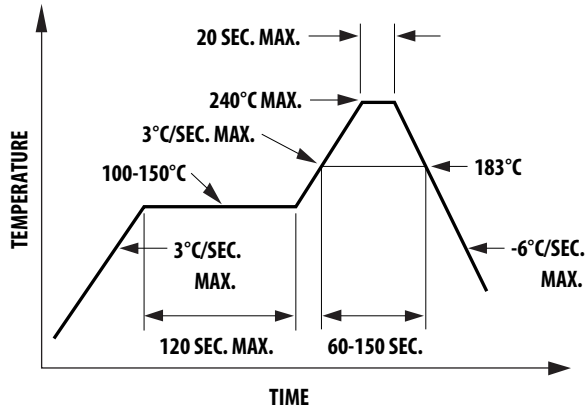


Note:  
 There is no color bin ID for Red as there is only one range, as stated in Table 4.

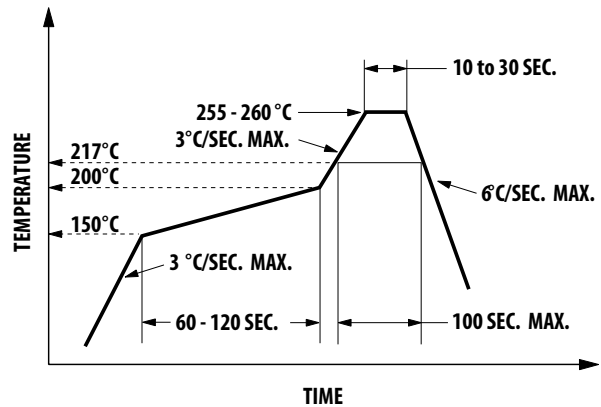
## Soldering

Recommended reflow soldering condition:

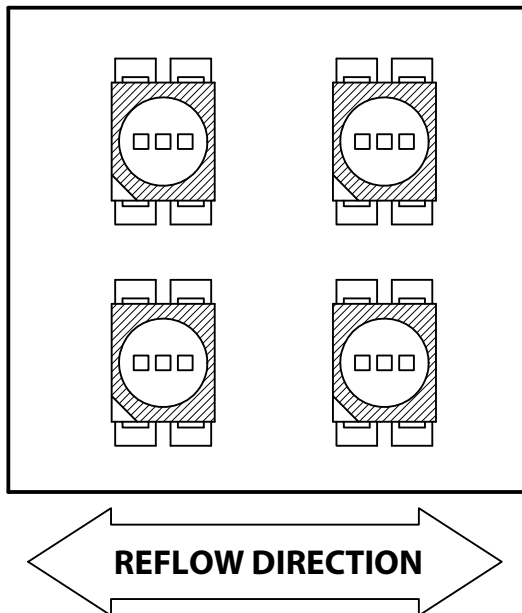
### (i) Leaded reflow soldering:



### (ii) Lead-free reflow soldering:



- Reflow soldering must not be done more than two times. Make sure you take the necessary precautions for handling a moisture-sensitive device, as stated in the following section.
- Recommended board reflow direction:



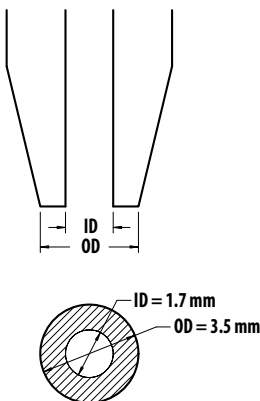
- Do not apply any pressure or force on the LED during reflow and after reflow when the LED is still hot.
- It is preferred that you use reflow soldering to solder the LED. Use hand soldering only for rework if unavoidable but must be strictly controlled to the following conditions:
  - Soldering iron tip temperature = 320 °C max.
  - Soldering duration = 3 sec max.
  - Number of cycles = 1 only
  - Power of soldering iron = 50 W max.
- Do not touch the LED body with a hot soldering iron except the soldering terminals as this may damage the LED.
- For de-soldering, it is recommended that you use a double flat tip.
- Please confirm beforehand whether the functionality and performance of the LED is affected by hand soldering.

## PRECAUTIONARY NOTES

### 1. Handling precautions

The encapsulation material of the LED is made of silicone for better product reliability. Compared to epoxy encapsulant, which is hard and brittle, silicone is softer and flexible. Observe special handling precautions during assembly of silicone-encapsulated LED products. Failure to comply might lead to damage and premature failure of the LED. For more information, refer to Application Note AN5288, *Silicone Encapsulation for LED: Advantages and Handling Precautions*.

- Do not poke sharp objects into the silicone encapsulant. Sharp objects such as tweezers and syringes might cause excessive force to be applied or even pierce through the silicone, inducing failures in the LED die or wire bond.
- Do not touch the silicone encapsulant. Uncontrolled force acting on the silicone encapsulant might result in excessive stress on the wire bond. The LED should be held only by the body.
- Do not stack assembled PCBs together. Use an appropriate rack to hold the PCBs.
- The surface of silicone material attracts more dust and dirt compared to epoxy due to its surface tackiness. To remove foreign particles on the surface of silicone, a cotton bud can be used with isopropyl alcohol (IPA). During cleaning, rub the surface gently without applying excessive pressure on the silicone. Ultrasonic cleaning is not recommended.
- For automated pick and place, Avago has tested the following nozzle size to work fine with this LED. However, due to possible variations in other parameters such as pick and place machine maker/model and other settings of the machine, it is recommended that you verify that the nozzle selected will not damage the LED.



### 2. Handling of moisture-sensitive device

This product has a Moisture Sensitive Level 3 rating per JEDEC J-STD-020. Refer to Avago Application Note AN5305, *Handling of Moisture Sensitive Surface Mount Devices*, for additional details and a review of proper handling procedures.

- Before use
  - An unopened moisture barrier bag (MBB) can be stored at  $< 40\text{ }^{\circ}\text{C}/90\%\text{ RH}$  for 12 months. If the actual shelf life has exceeded 12 months and the Humidity Indicator Card (HIC) indicates that baking is not required, then it is safe to reflow the LEDs per the original MSL rating.
  - It is recommended that the MBB not be opened before assembly (e.g., for IQC).
- Control after opening the MBB
  - Read the HIC immediately upon opening the MBB.
  - The LEDs must be kept at  $< 30\text{ }^{\circ}\text{C}/60\%\text{ RH}$  at all times and all high temperature related processes including soldering, curing or rework must be completed within 168 hours.
- Control for unfinished reel
  - Unused LEDs must be stored in a sealed MBB with desiccant or desiccator at  $< 5\%\text{ RH}$ .
- Control of assembled boards
  - If the PCB soldered with the LEDs is to be subjected to other high temperature processes, then the PCB must be stored in a sealed MBB with desiccant or desiccator at  $< 5\%\text{ RH}$  to ensure that all LEDs have not exceeded their floor life of 168 hours.
- Baking is required if:
  - The HIC indicator is not BROWN at 10% and is AZURE at 5%.
  - The LEDs are exposed to a condition of  $> 30\text{ }^{\circ}\text{C}/60\%\text{ RH}$  at any time.
  - The LED floor life exceeded 168 hours.

The recommended baking condition is:  $60 \pm 5\text{ }^{\circ}\text{C}$  for 20 hours.

Baking should be done only once.
- Storage
  - The soldering terminals of these Avago LEDs are silver-plated. If the LEDs are exposed in ambient environment for too long, the silver plating might be oxidized and thus affect its solderability performance. As such, unused LEDs must be kept in a sealed MBB with desiccant or in desiccator at  $< 5\%\text{ RH}$ .

### 3. Application precautions

- Drive current of the LED must not exceed the maximum allowable limit across temperature as stated in the datasheet. Constant current driving is recommended to ensure consistent performance.
- The LED is not intended for reverse bias. Do use other appropriate components for such a purpose. When driving the LED in matrix form, make sure the reverse bias voltage does not exceed the allowable limit for the LED.
- Do not use the LED in the vicinity of material with sulfur content, in an environment of high gaseous sulfur compound and corrosive elements. Examples of materials that may contain sulfur are rubber gasket, Room Temperature Vulcanizing (RTV) silicone rubber, rubber gloves, and so on. Prolonged exposure to such an environment may affect the optical characteristics and product life.
- Avoid a rapid change in ambient temperature especially in a high humidity environment as this will cause condensation on the LED.

### 4. Thermal management

Optical, electrical and reliability characteristics of LED are affected by temperature. The junction temperature ( $T_J$ ) of the LED must be kept below the allowable limit at all times.  $T_J$  can be calculated as follows:

$$T_J = T_A + R_{\theta J-A} \times I_F \times V_{Fmax}$$

where

$T_A$  = ambient temperature [ $^{\circ}\text{C}$ ]

$R_{\theta J-A}$  = thermal resistance from LED junction to ambient [ $^{\circ}\text{C}/\text{W}$ ]

$I_F$  = forward current [A]

$V_{Fmax}$  = maximum forward voltage [V]

The complication of using this formula lies in  $T_A$  and  $R_{\theta J-A}$ . Actual  $T_A$  is sometimes subjective and hard to determine.  $R_{\theta J-A}$  varies from system to system depending on design and is usually not known.

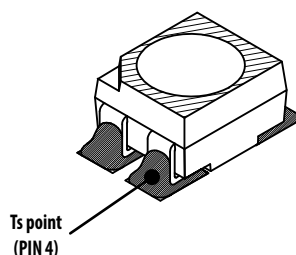
Another way of calculating  $T_J$  is by using solder point temperature  $T_S$  as shown as follows:

$$T_J = T_S + R_{\theta J-S} \times I_F \times V_{Fmax}$$

where

$T_S$  = LED solder point temperature as shown in the following illustration [ $^{\circ}\text{C}$ ]

$R_{\theta J-S}$  = thermal resistance from junction to solder point [ $^{\circ}\text{C}/\text{W}$ ]



$T_S$  can be easily measured by having a thermocouple mounted on the soldering joint, as shown in this illustration, while  $R_{\theta J-S}$  is provided in the datasheet. Please verify the  $T_S$  of the LED in the final product to ensure that the LEDs are operated within all maximum ratings stated in the datasheet.

### 5. Eye safety precautions

LEDs may pose optical hazards when in operation. It is not advisable to view directly at operating LEDs as it may be harmful to the eyes. For safety reasons, use appropriate shielding or personal protective equipment.

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