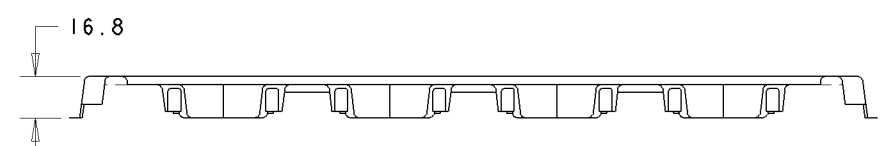
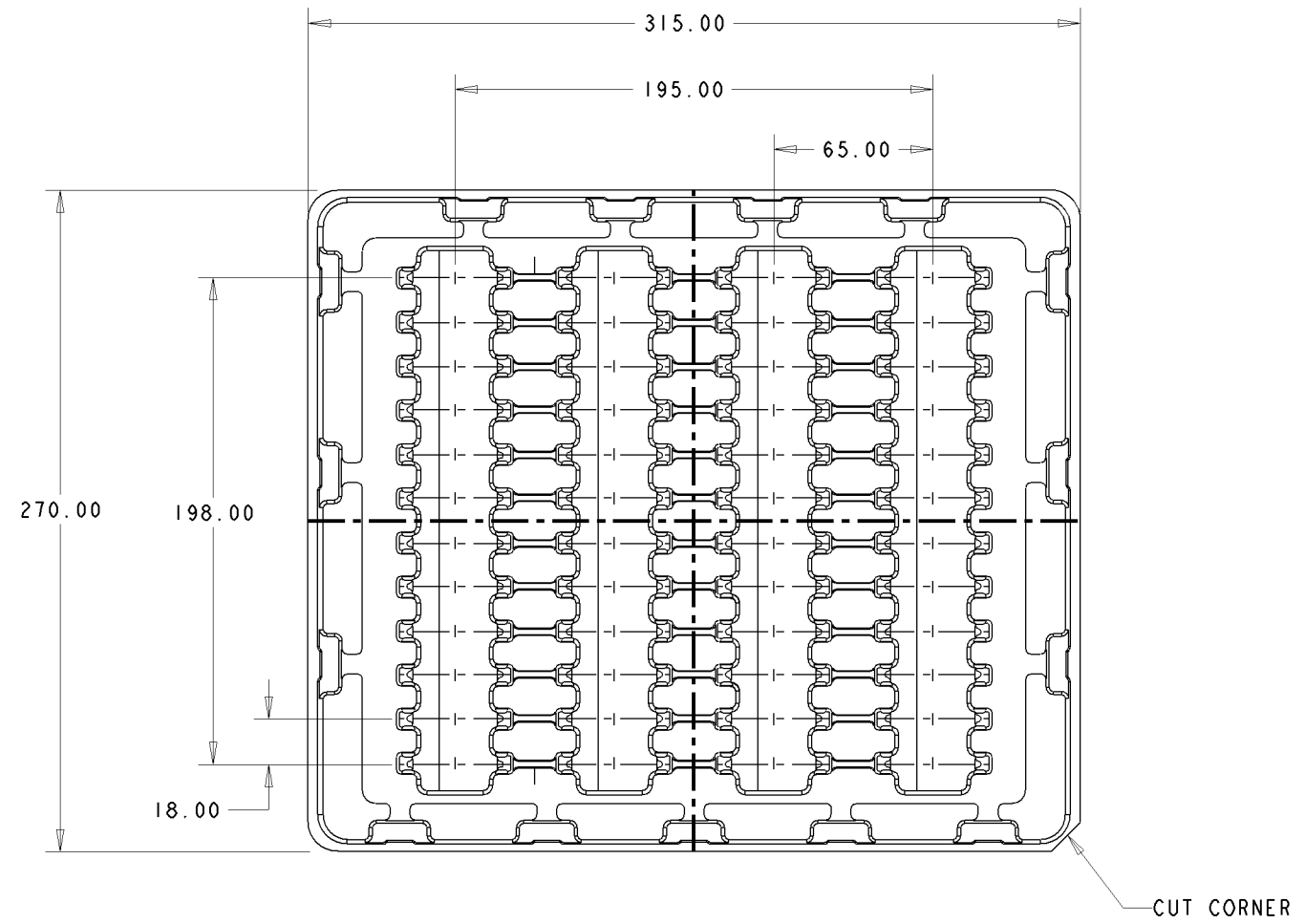
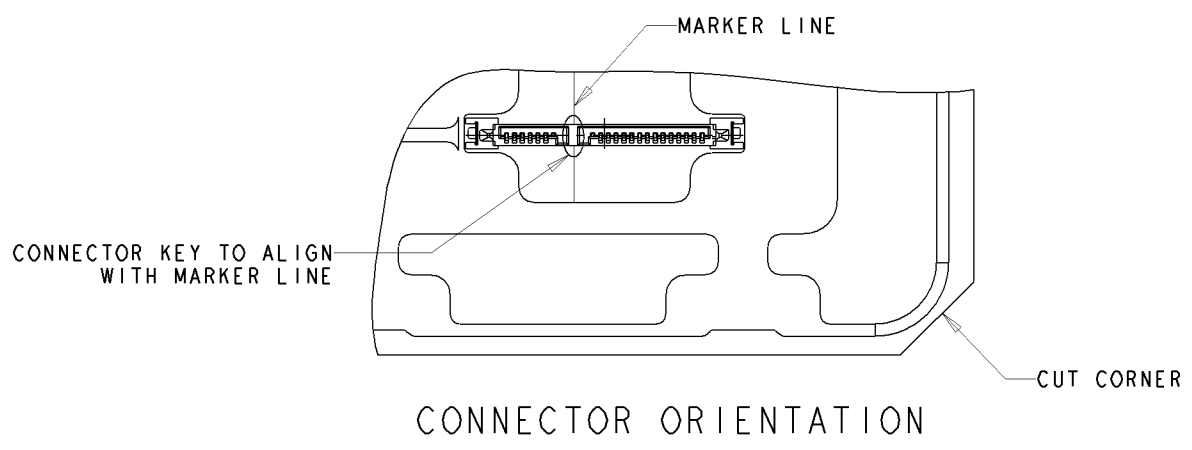
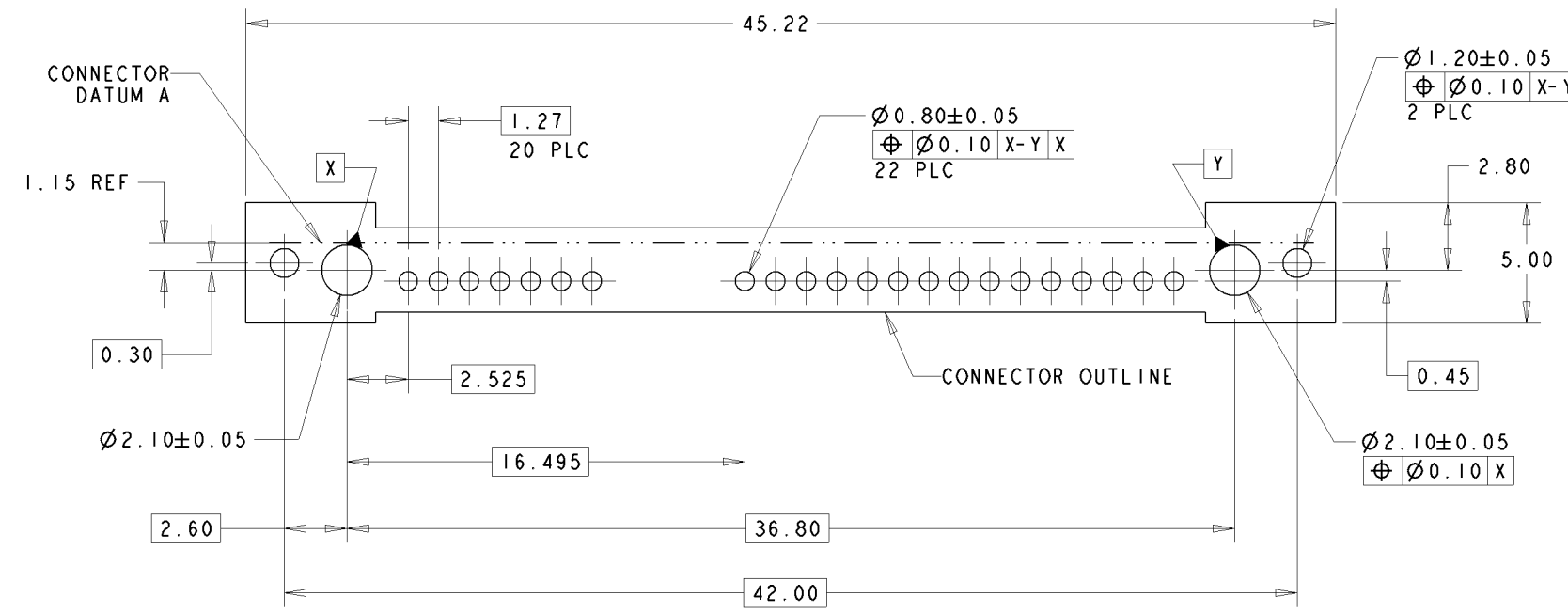


LOC	DIST	REVISIONS			
P	LTR	DESCRIPTION	DATE	DWN	APVD
DY	-	SEE SHEET 1	-	-	-



PACKAGING TRAY
 QTY/TRAY: 48PCS



RECOMMENDED PCB LAYOUT
 SCALE 5:1

THIS DRAWING IS A CONTROLLED DOCUMENT FOR AMP INCORPORATED. IT IS SUBJECT TO CHANGE AND THE CONTROLLING ENGINEERING ORGANIZATION SHOULD BE CONTACTED FOR THE LATEST REVISION.		DWN SK LEAW 15JAN2007	Tyco Electronics Singapore Pte. Ltd.	
DIMENSIONS - mm		CHK SF LEONG 14FEB2007	NAME SATA BACKPLANE RECEPTACLE, VERTICAL, THROUGH HOLE	
TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ± 1 PLC ± 2 PLC ±0.20 3 PLC ± 4 PLC ± ANGLES ±3° FINISH -		APVD SF LEONG 14FEB2007	PRODUCT SPEC -	SIZE A2
MATERIAL -		APPLICATION SPEC -	WEIGHT -	CAGE CODE 00779
CUSTOMER DRAWING			DRAWING NO C=1735397	SCALE 4:1 SHEET 2 of 2 REV A