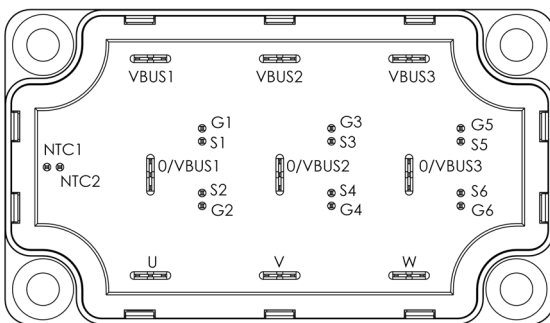
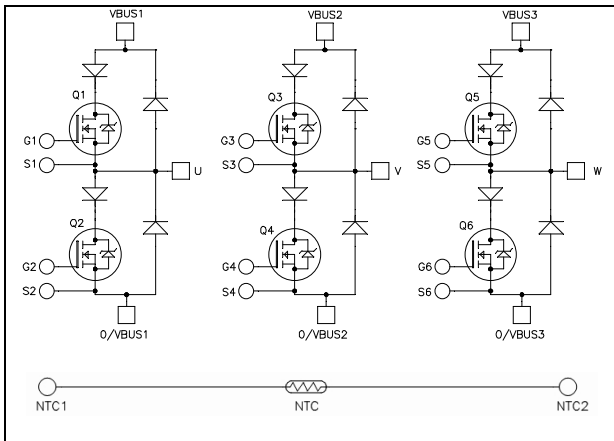


## Triple phase leg CoolMOS™ Power Module

$V_{DSS} = 600V$   
 $R_{DSon} = 21m\Omega \text{ typ @ } T_j = 25^\circ C$   
 $I_D = 116A \text{ @ } T_c = 25^\circ C$



### Application

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Motor control


### Features

- **CoolMOS™**
  - Ultra low  $R_{DSon}$
  - Low Miller capacitance
  - Ultra low gate charge
  - Avalanche energy rated
  - Very rugged
- **SiC Parallel Schottky Diode**
  - Zero reverse recovery
  - Zero forward recovery
  - Temperature Independent switching behavior
  - Positive temperature coefficient on VF
- Kelvin source for easy drive
- Very low stray inductance
  - Symmetrical design
  - Lead frames for power connections
- High level of integration
- Internal thermistor for temperature monitoring
- AIN substrate for improved thermal performance

### Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Very low (12mm) profile
- Each leg can be easily paralleled to achieve a phase leg of three times the current capability
- Module can be configured as a three phase bridge
- RoHS Compliant

**All ratings @  $T_j = 25^\circ C$  unless otherwise specified**


**CAUTION:** These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on [www.microsemi.com](http://www.microsemi.com)

**Absolute maximum ratings** (Per CoolMOS™)

<b>Symbol</b>	<b>Parameter</b>	<b>Max ratings</b>	<b>Unit</b>
V <sub>DSS</sub>	Drain - Source Breakdown Voltage	600	V
I <sub>D</sub>	Continuous Drain Current	T <sub>c</sub> = 25°C	116
		T <sub>c</sub> = 80°C	87
I <sub>DM</sub>	Pulsed Drain current	400	A
V <sub>GS</sub>	Gate - Source Voltage	±20	V
R <sub>DS(on)</sub>	Drain - Source ON Resistance	21	mΩ
P <sub>D</sub>	Maximum Power Dissipation	T <sub>c</sub> = 25°C	625
I <sub>AR</sub>	Avalanche current (repetitive and non repetitive)	13	A
E <sub>AR</sub>	Repetitive Avalanche Energy	3	mJ
E <sub>AS</sub>	Single Pulse Avalanche Energy	1950	

**Electrical Characteristics** (Per CoolMOS™)

<b>Symbol</b>	<b>Characteristic</b>	<b>Test Conditions</b>	<b>Min</b>	<b>Typ</b>	<b>Max</b>	<b>Unit</b>
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 600V			200	μA
R <sub>DS(on)</sub>	Drain – Source on Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 88A		18.5	21	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 6mA	2.4	3	3.6	V
I <sub>GSS</sub>	Gate – Source Leakage Current	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0V			200	nA

**Dynamic Characteristics** (Per CoolMOS™)

<b>Symbol</b>	<b>Characteristic</b>	<b>Test Conditions</b>	<b>Min</b>	<b>Typ</b>	<b>Max</b>	<b>Unit</b>
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V ; V <sub>DS</sub> = 100V f = 1MHz		13		nF
C <sub>oss</sub>	Output Capacitance			0.72		
Q <sub>g</sub>	Total gate Charge	V <sub>GS</sub> = 10V V <sub>Bus</sub> = 480V I <sub>D</sub> = 88A		580		nC
Q <sub>gs</sub>	Gate – Source Charge			72		
Q <sub>gd</sub>	Gate – Drain Charge			300		
T <sub>d(on)</sub>	Turn-on Delay Time	<b>Inductive Switching @ 25°C</b> V <sub>GS</sub> = 13V V <sub>Bus</sub> = 400V I <sub>D</sub> = 88A R <sub>G</sub> = 0.8Ω		23		ns
T <sub>r</sub>	Rise Time			10		
T <sub>d(off)</sub>	Turn-off Delay Time			130		
T <sub>f</sub>	Fall Time			7		
E <sub>on</sub>	Turn-on Switching Energy	<b>Inductive switching @ 125°C</b> V <sub>GS</sub> = 13V, V <sub>Bus</sub> = 400V I <sub>D</sub> = 88A, R <sub>G</sub> = 0.8Ω		1.2		mJ
E <sub>off</sub>	Turn-off Switching Energy			2.8		
R <sub>thJC</sub>	Junction to Case Thermal Resistance				0.20	°C/W

**Series diode ratings and characteristics** (Per series diode)

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
V <sub>RRM</sub>	Maximum Repetitive Reverse Voltage		600			V
I <sub>RM</sub>	Maximum Reverse Leakage Current	V <sub>R</sub> = 600V			100	μA
I <sub>F</sub>	DC Forward Current			75		A
V <sub>F</sub>	Diode Forward Voltage	I <sub>F</sub> = 75A	T <sub>j</sub> = 25°C	1.6	2	V
			T <sub>j</sub> = 150°C	1.5		
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> = 75A V <sub>R</sub> = 300V di/dt = 2000A/μs	T <sub>j</sub> = 25°C	100		ns
			T <sub>j</sub> = 150°C	150		
Q <sub>rr</sub>	Reverse Recovery Charge	I <sub>F</sub> = 75A V <sub>R</sub> = 300V di/dt = 2000A/μs	T <sub>j</sub> = 25°C	3.6		nC
			T <sub>j</sub> = 150°C	7.6		
R <sub>thJC</sub>	Junction to Case Thermal Resistance				0.80	°C/W

**SiC Parallel diode ratings and characteristics** (Per parallel diode)

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
V <sub>RRM</sub>	Maximum Peak Repetitive Reverse Voltage		600			V
I <sub>RM</sub>	Maximum Reverse Leakage Current	V <sub>R</sub> = 600V	T <sub>j</sub> = 25°C	30	180	μA
			T <sub>j</sub> = 175°C	60	900	
I <sub>F</sub>	DC Forward Current			30		A
V <sub>F</sub>	Diode Forward Voltage	I <sub>F</sub> = 30A	T <sub>j</sub> = 25°C	1.6	1.8	V
			T <sub>j</sub> = 175°C	2	2.4	
Q <sub>C</sub>	Total Capacitive Charge	I <sub>F</sub> = 30A, V <sub>R</sub> = 600V di/dt = 1000A/μs		84		nC
C	Total Capacitance	f = 1MHz, V <sub>R</sub> = 200V		195		pF
		f = 1MHz, V <sub>R</sub> = 400V		150		
R <sub>thJC</sub>	Junction to Case Thermal Resistance				0.80	°C/W

**Thermal and package characteristics**

Symbol	Characteristic	Min	Typ	Max	Unit	
V <sub>ISOL</sub>	RMS Isolation Voltage, any terminal to case t = 1 min, 50/60Hz	4000			V	
T <sub>J</sub>	Operating junction temperature range	-40		150*	°C	
T <sub>STG</sub>	Storage Temperature Range	-40		125		
T <sub>C</sub>	Operating Case Temperature	-40		100		
Torque	Mounting torque	To heatsink	M6	3	5	N.m
Wt	Package Weight				250	g

\* T<sub>j</sub> = 175°C for series and parallel diodes

**Temperature sensor NTC** (see application note APT0406 on www.microsemi.com for more information).

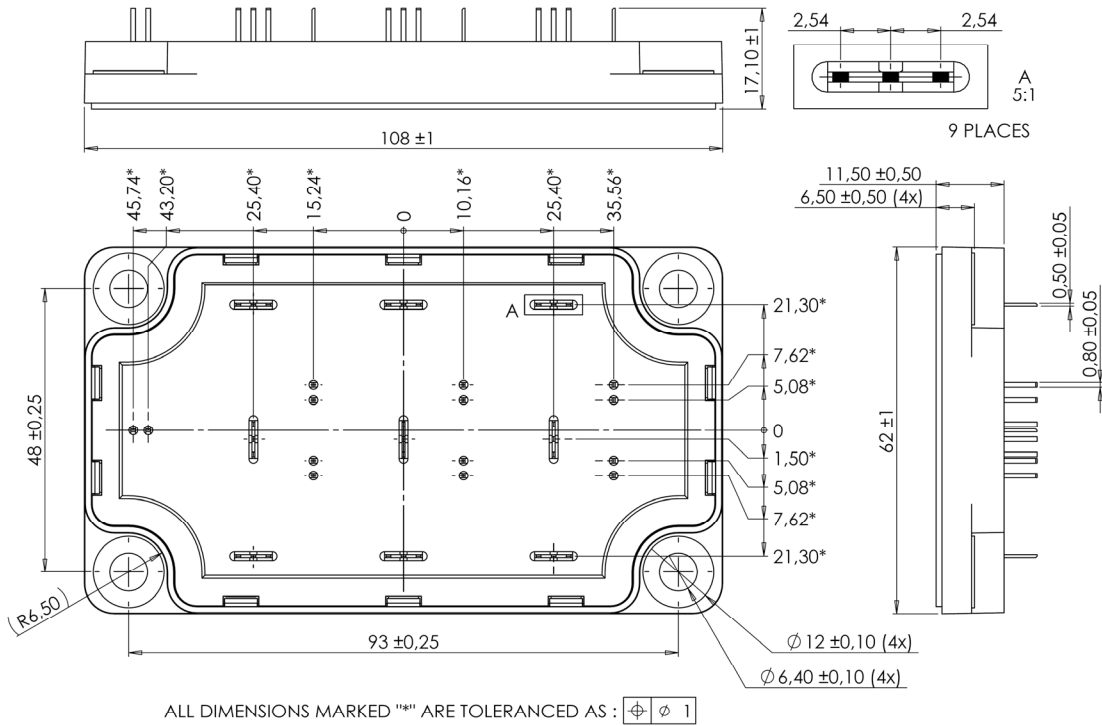
Pins NTC1 & NTC2 are only mounted on APTM100TA35SCTPG power module.

Symbol	Characteristic	Min	Typ	Max	Unit
R <sub>25</sub>	Resistance @ 25°C		50		kΩ
ΔR <sub>25</sub> /R <sub>25</sub>			5		%
B <sub>25/85</sub>	T <sub>25</sub> = 298.15 K		3952		K
ΔB/B		T <sub>C</sub> = 100°C	4		%

$$R_T = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T} - \frac{1}{T_{25}}\right)\right]}$$

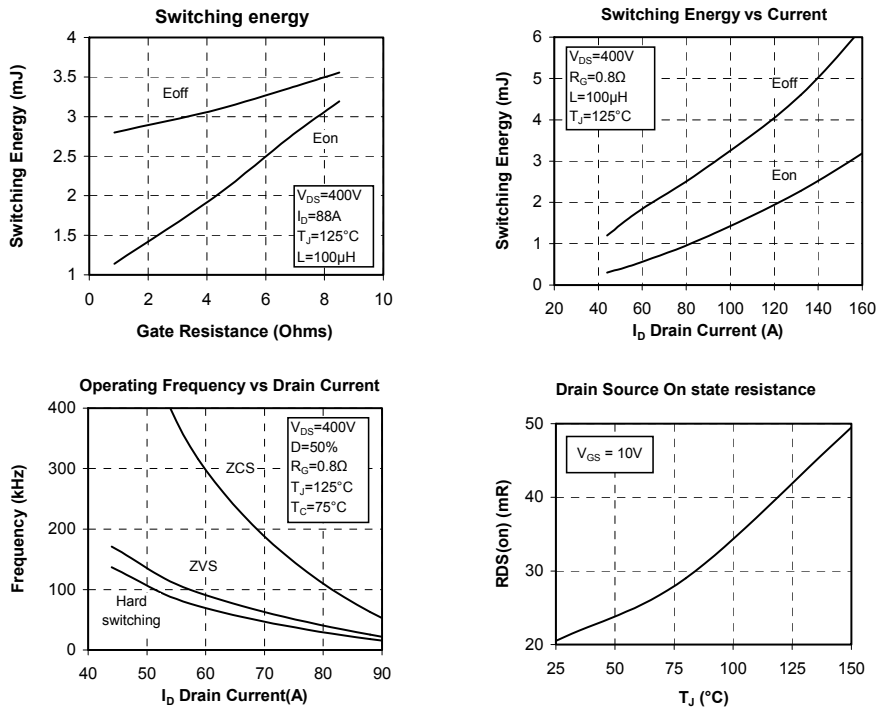
T: Thermistor temperature  
 R<sub>T</sub>: Thermistor value at T

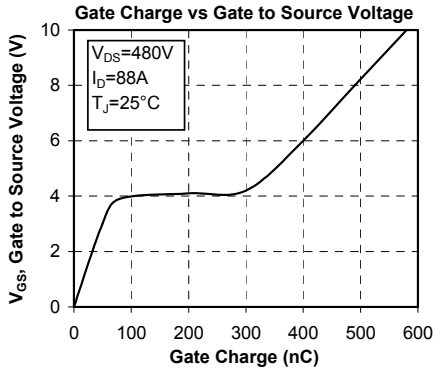
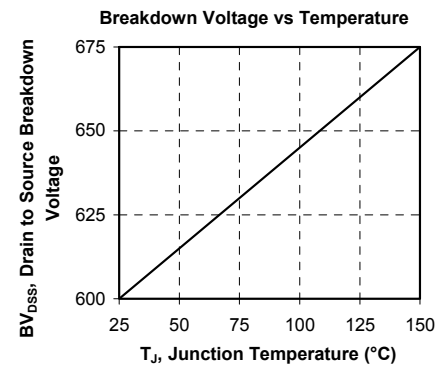
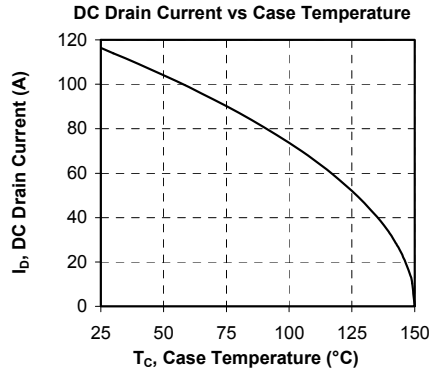
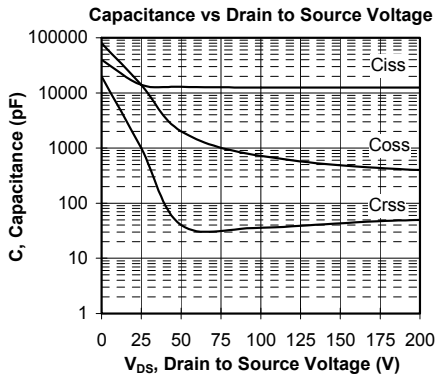
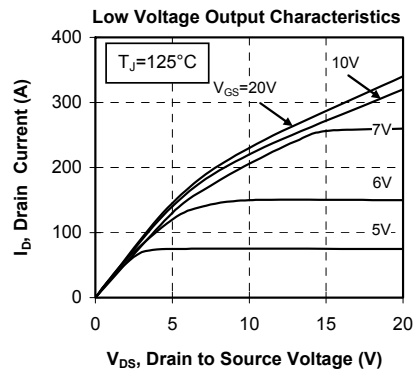
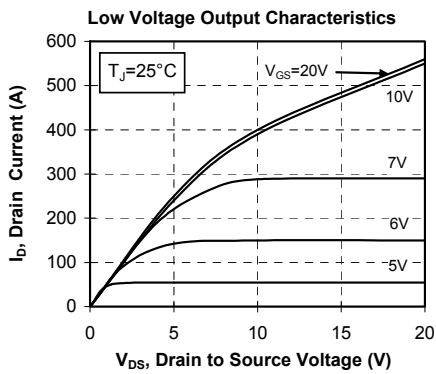
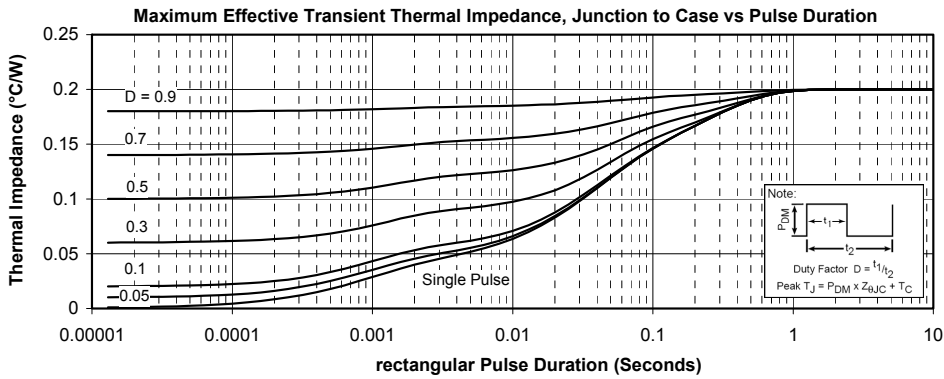
**SP6-P Package outline (dimensions in mm)**



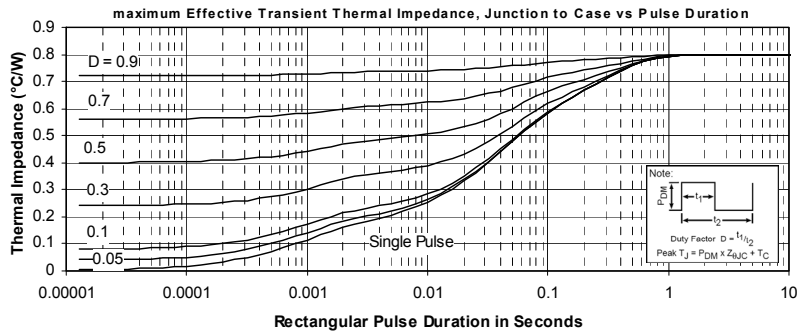
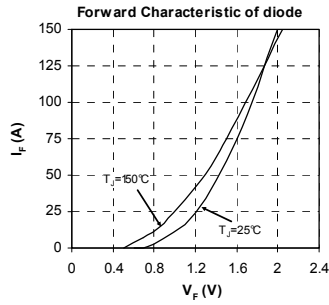
See application note 1902 - Mounting Instructions for SP6-P (12mm) Power Modules on [www.microsemi.com](http://www.microsemi.com)

## Typical CoolMOS™ Performance Curve

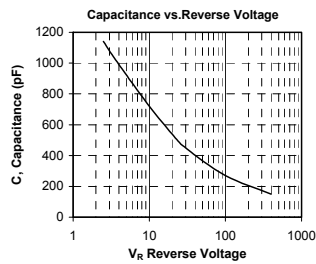
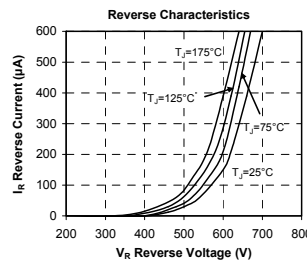
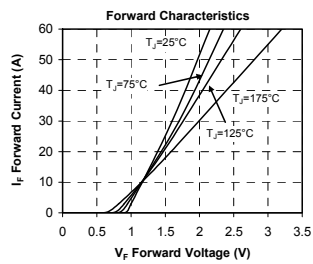
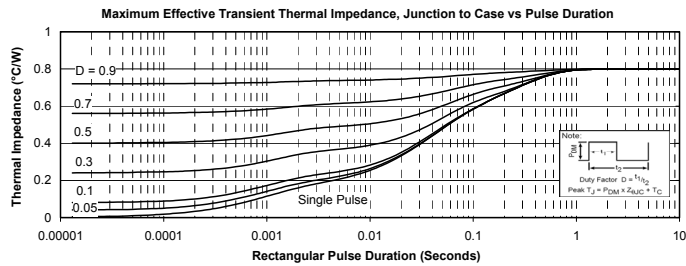




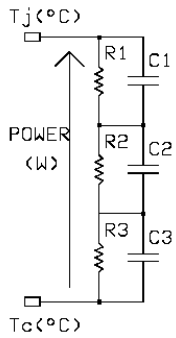
**Typical series diode Performance Curve**



**Typical SiC parallel diode Performance Curve**



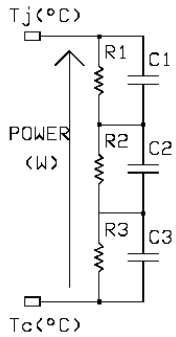
**Thermal impedance ; CoolMOS™**



**RC Final Model**

- R1 = 0.044 Ω
- R2 = 0.103 Ω
- R3 = 0.053 Ω
- C1 = 0.022 F
- C2 = 0.347 F
- C3 = 4.31 F

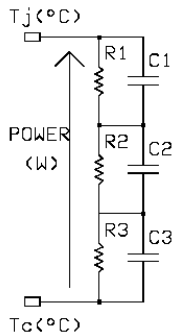
**Thermal impedance ; Series diode**



**RC Final Model**

- R1 = 0.176 Ω
- R2 = 0.413 Ω
- R3 = 0.211 Ω
- C1 = 0.0055 F
- C2 = 0.086 F
- C3 = 1.07 F

**Thermal impedance ; SiC Parallel diode**



**RC Final Model**

- R1 = 0.176 Ω
- R2 = 0.413 Ω
- R3 = 0.211 Ω
- C1 = 0.0055 F
- C2 = 0.086 F
- C3 = 1.07 F

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